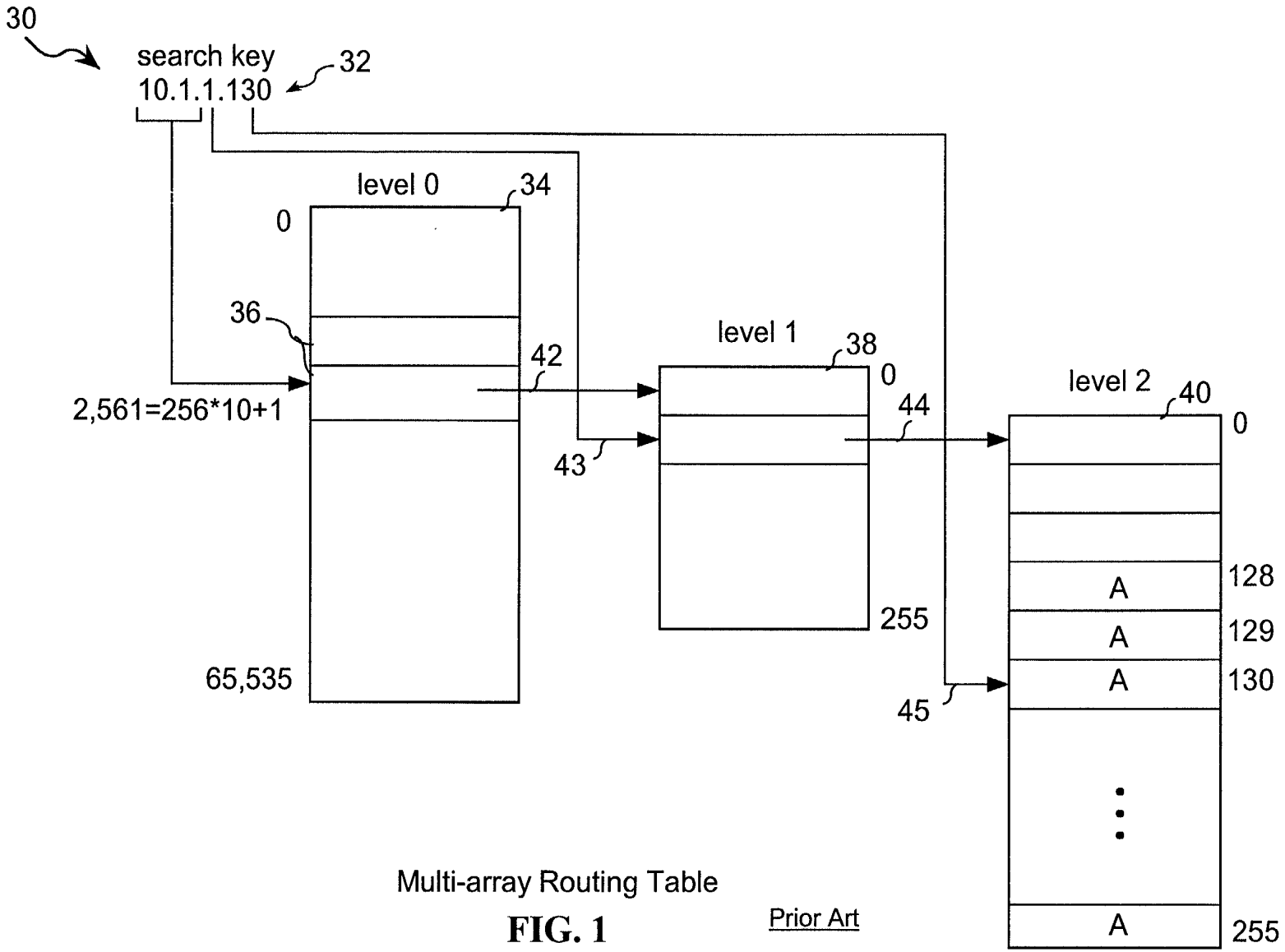


Multi-array Routing Table

FIG. 1

Prior Art

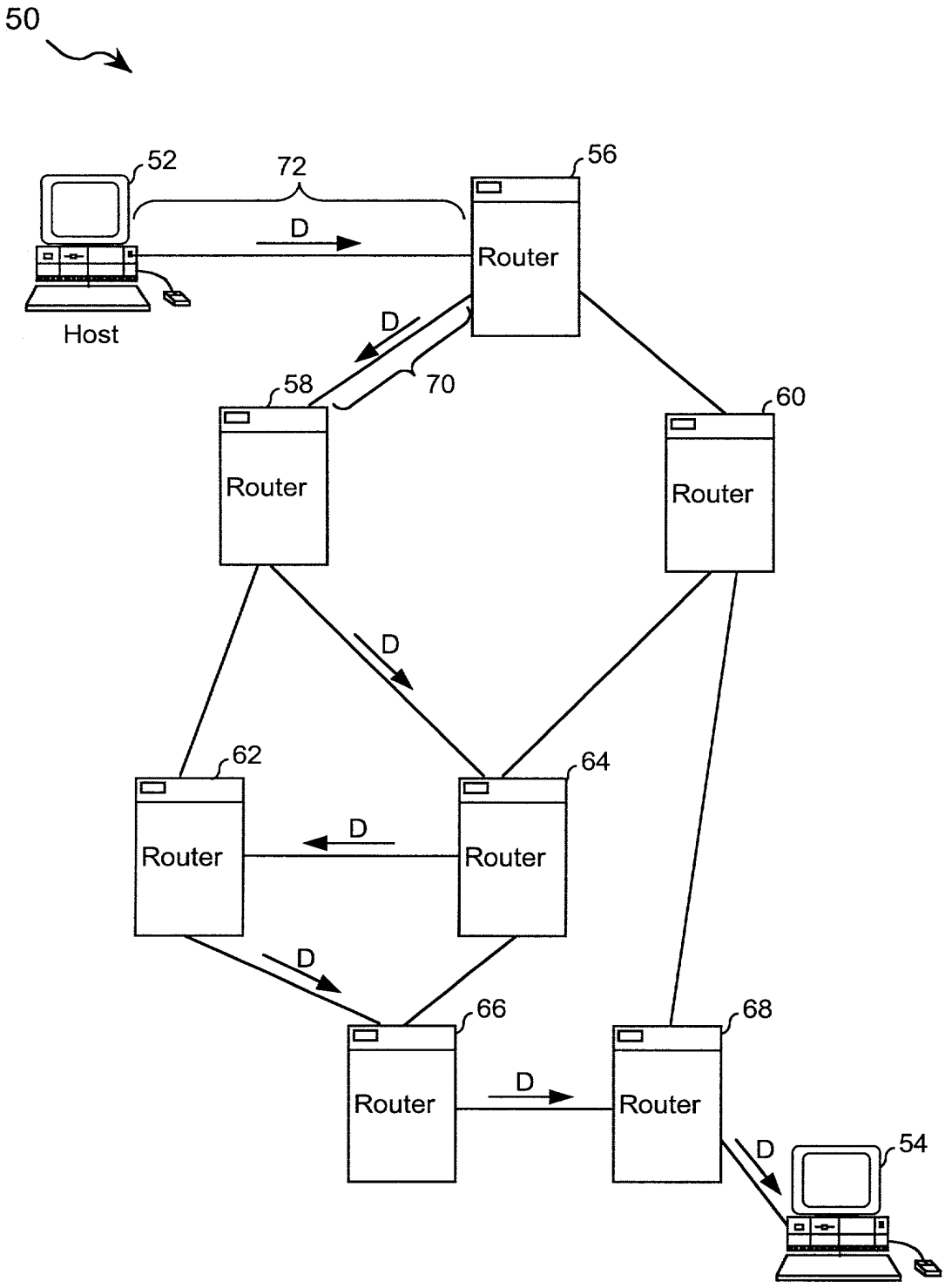

50
52
72
56
D
Router
Host
D
58
70
60
Router
Router
D
62
64
Router
D
Router
D
66
68
Router
D
Router
D
54

FIG. 2

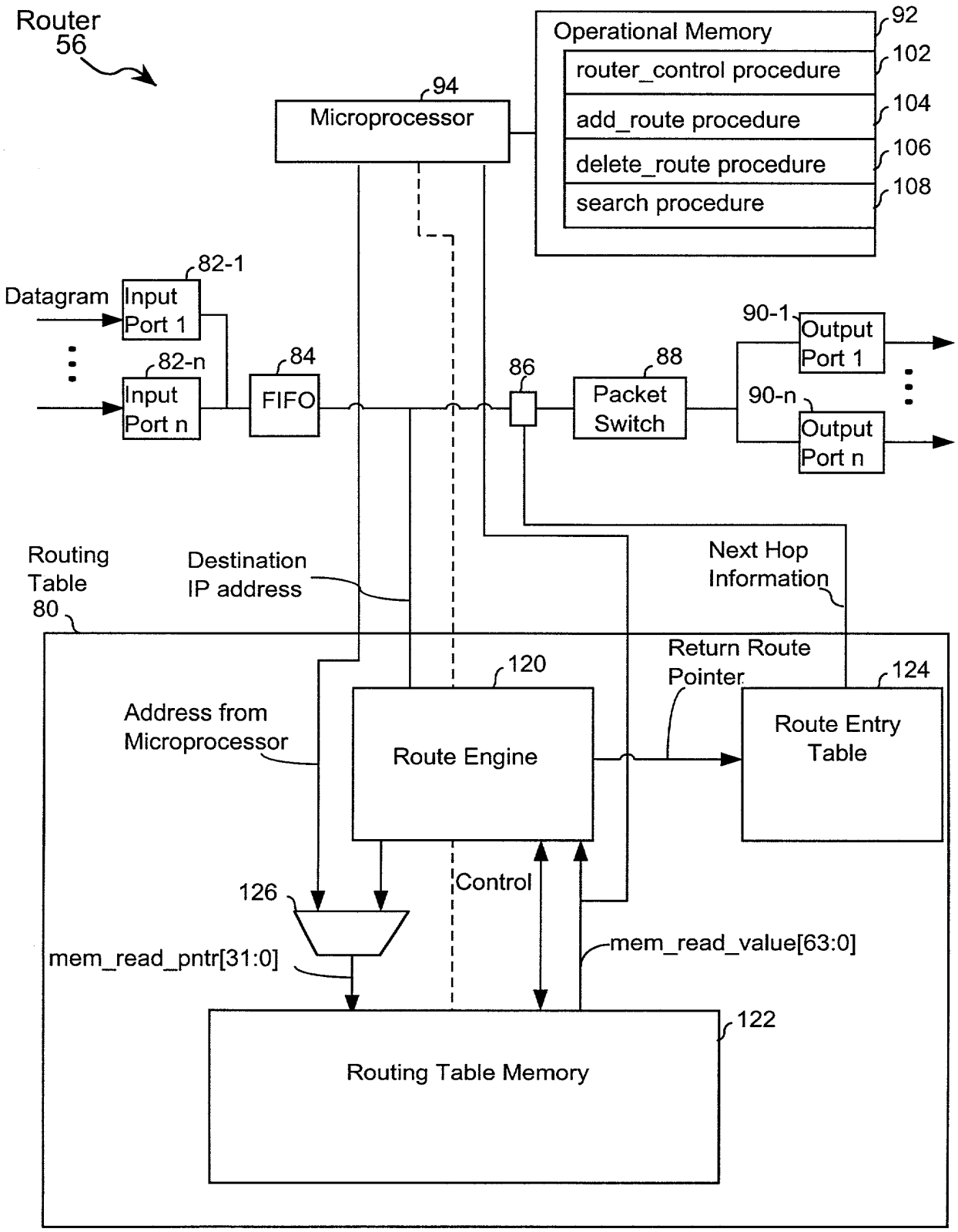

Router
56
Operational Memory
92
router_control procedure
102
add_route procedure
104
delete_route procedure
106
search procedure
108
Microprocessor
94
Datagram
Input Port 1
82-1
Input Port n
82-n
FIFO
84
86
Packet Switch
88
90-1
Output Port 1
90-n
Output Port n
Routing Table
80
Destination IP address
Next Hop Information
Address from Microprocessor
Route Engine
120
Return Route Pointer
Route Entry Table
124
126
Control
mem_read_pntr[31:0]
mem_read_value[63:0]
Routing Table Memory
122
Router

FIG. 3

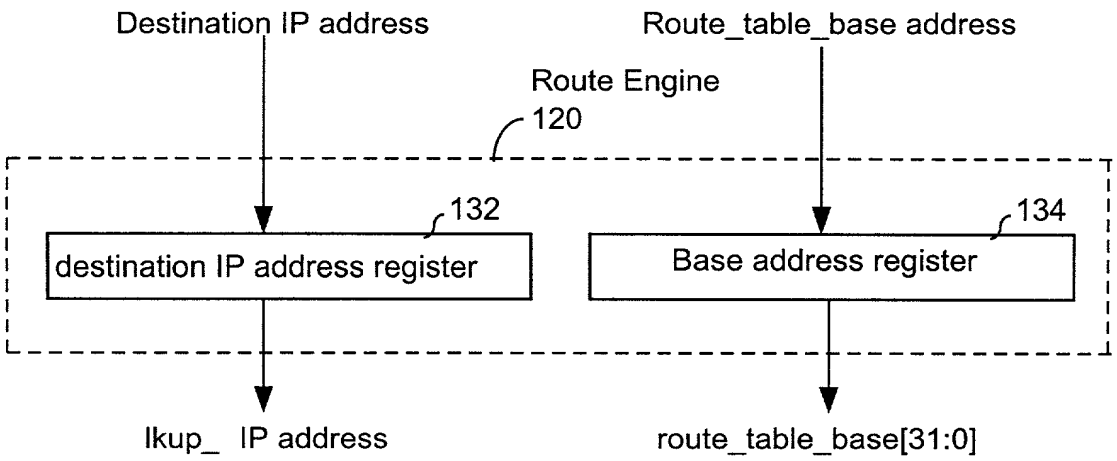

Destination IP address
Route_table_base address
Route Engine
120
132
134
destination IP address register
Base address register
Ikup_ IP address
route_table_base[31:0]

FIG. 4

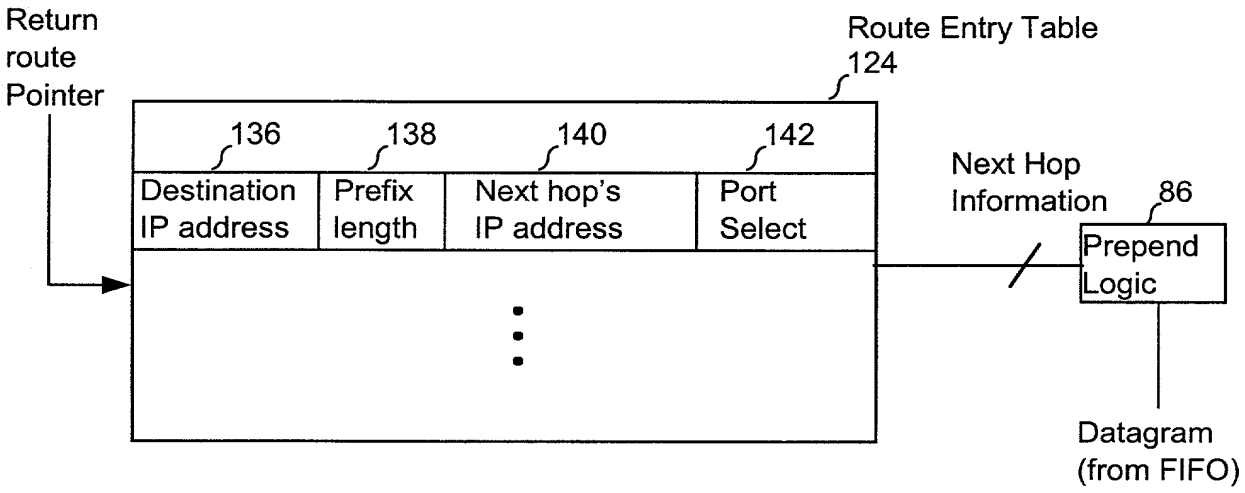

Return
route
Pointer
Route Entry Table
124
136
138
140
142
Destination
IP address
Prefix
length
Next hop's
IP address
Port
Select
Next Hop
Information
86
Prepend
Logic
Datagram
(from FIFO)

FIG. 5

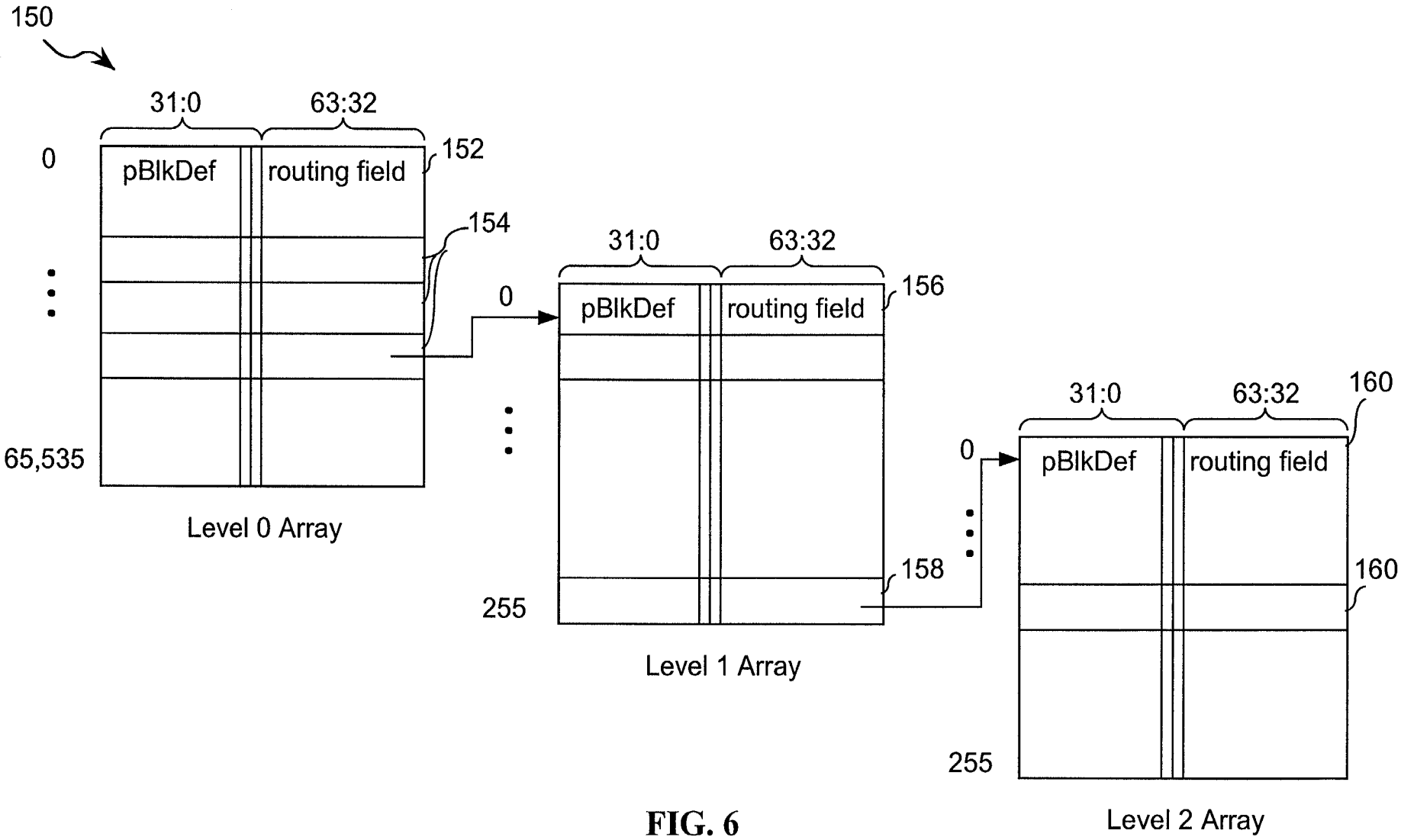


FIG. 6

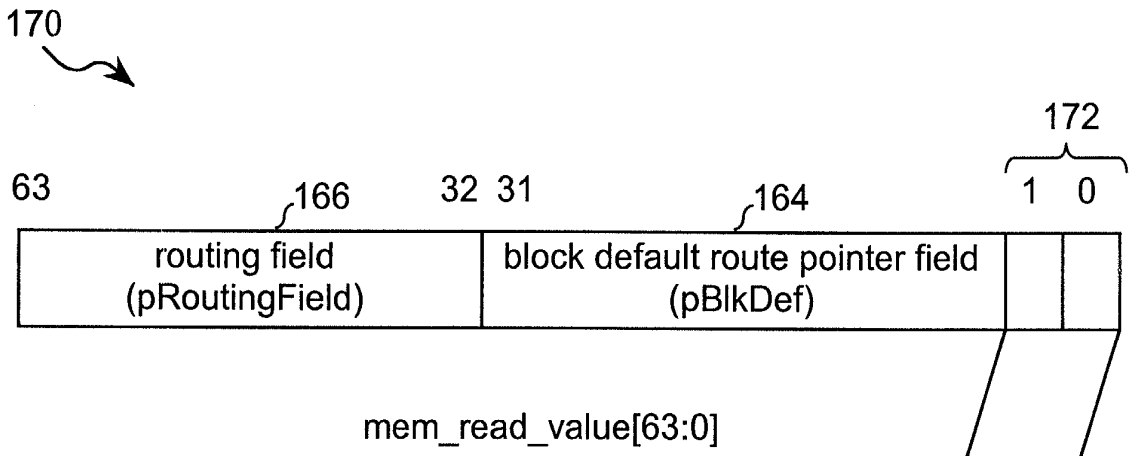

170
172
63
166
32 31
164
1 0
routing field
(pRoutingField)
block default route pointer field
(pBlkDef)
mem_read_value[63:0]

FIG. 7

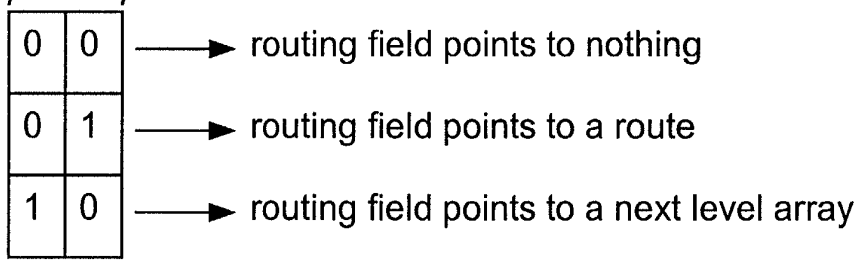

1 0
0 0
routing field points to nothing
0 1
routing field points to a route
1 0
routing field points to a next level array

FIG. 8

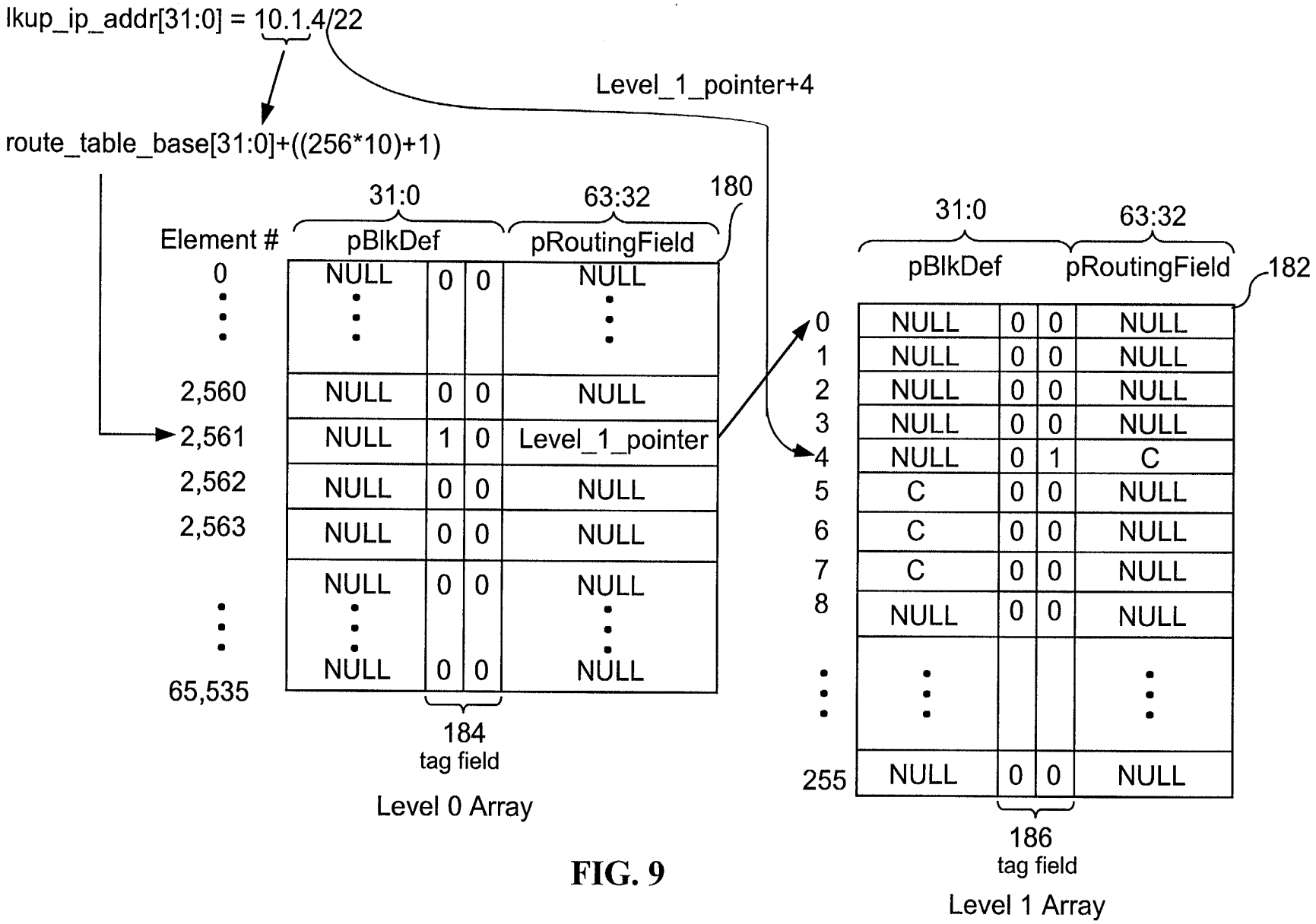

lkup_ip_addr[31:0] = 10.1.4/22
Level_1_pointer+4
route_table_base[31:0]+((256*10)+1)
180
31:0
63:32
Element #
pBlkDef
pRoutingField
0
2,560
2,561
2,562
2,563
65,535
NULL
Level_1_pointer
184
tag field
Level 0 Array
182
255
C
186
tag field
Level 1 Array

FIG. 9

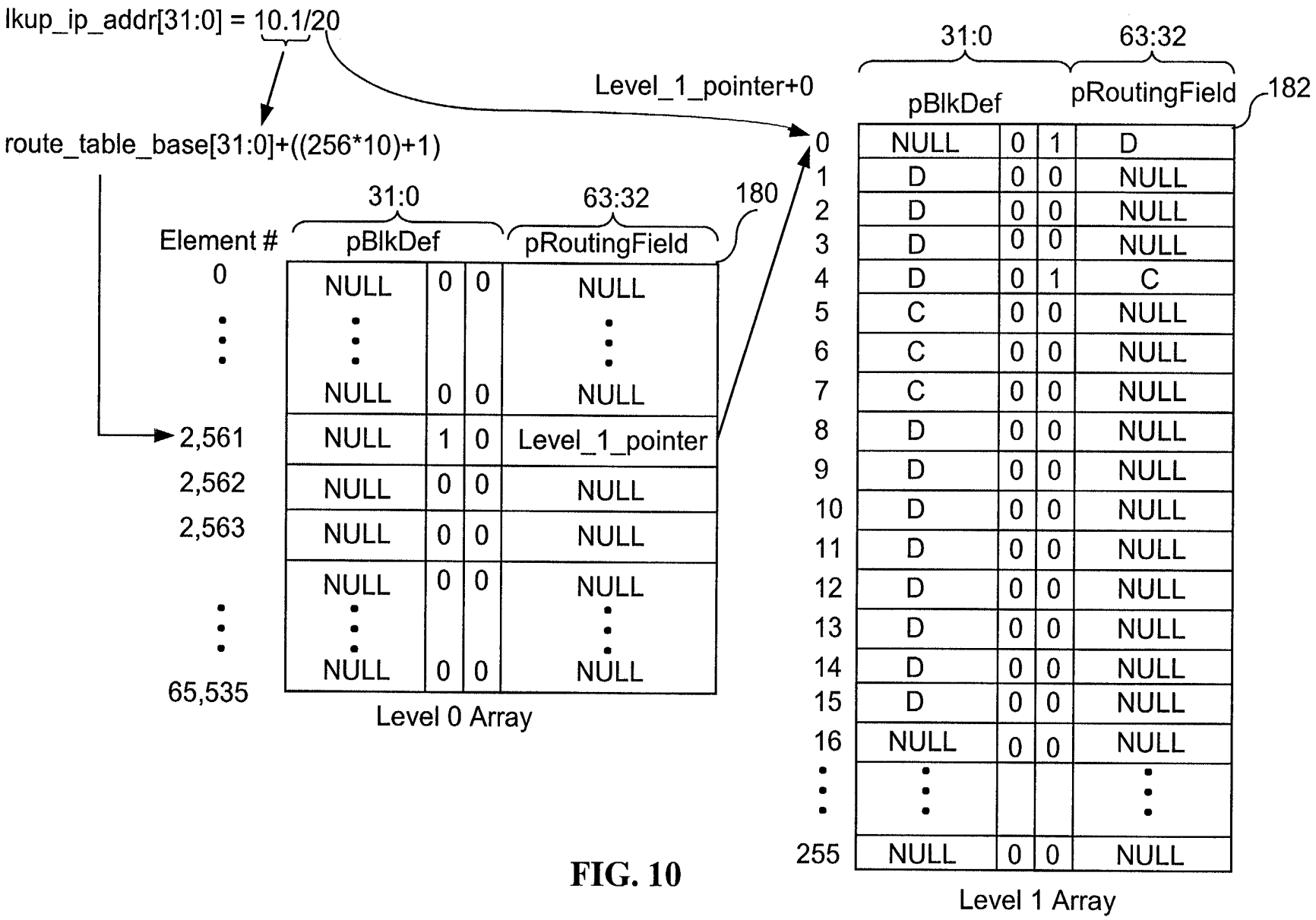

lkup_ip_addr[31:0] = 10.1/20
route_table_base[31:0]+((256*10)+1)
Level_1_pointer+0
31:0
63:32
180
Element #
pBlkDef
pRoutingField
0
2,561
2,562
2,563
65,535
NULL 0 0 NULL
NULL 1 0 Level_1_pointer
Level 0 Array
31:0
63:32
182
pBlkDef
pRoutingField
0 NULL 0 1 D
1 D 0 0 NULL
2 D 0 0 NULL
3 D 0 0 NULL
4 D 0 1 C
5 C 0 0 NULL
6 C 0 0 NULL
7 C 0 0 NULL
8 D 0 0 NULL
9 D 0 0 NULL
10 D 0 0 NULL
11 D 0 0 NULL
12 D 0 0 NULL
13 D 0 0 NULL
14 D 0 0 NULL
15 D 0 0 NULL
16 NULL 0 0 NULL
255 NULL 0 0 NULL
Level 1 Array

FIG. 10

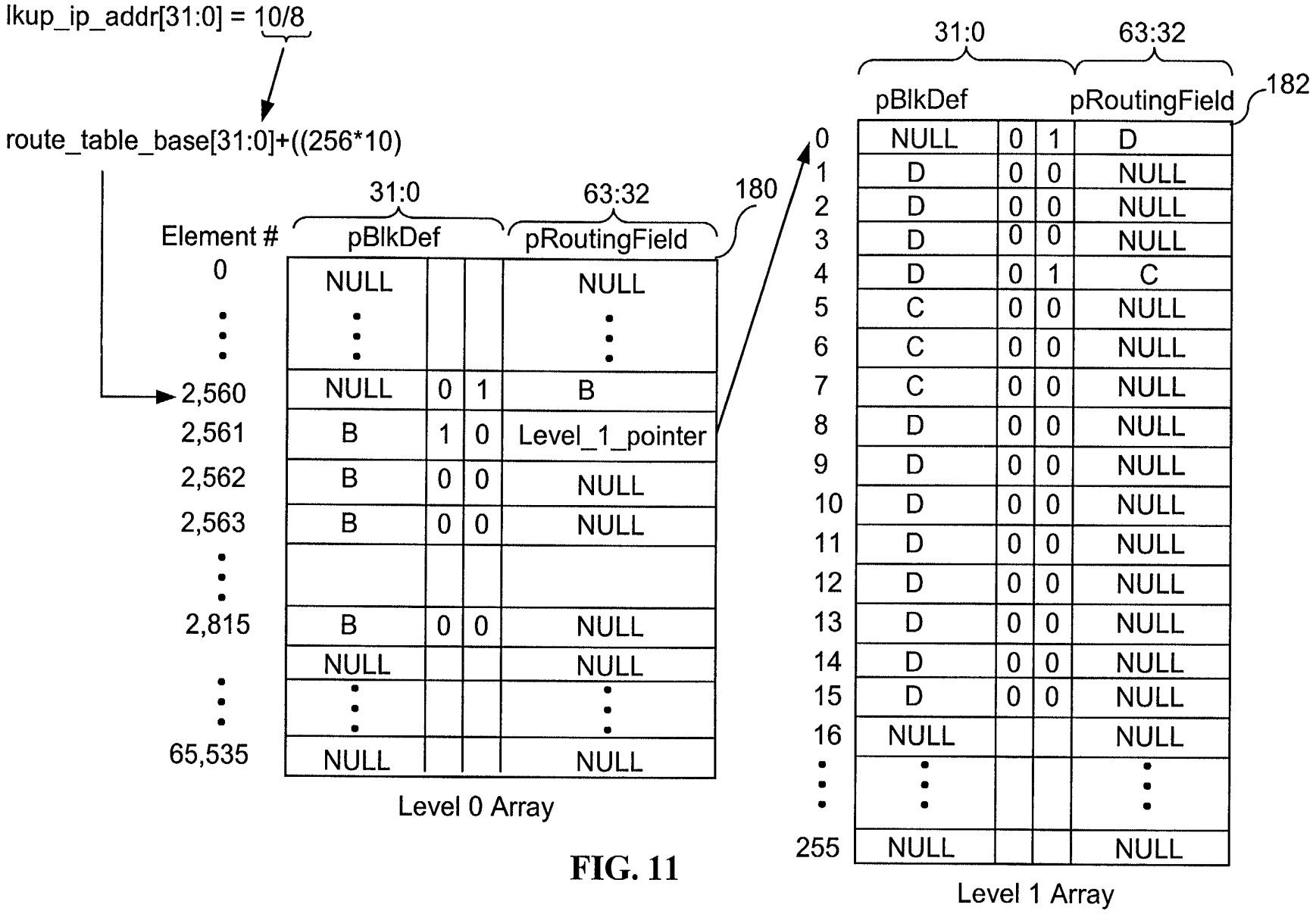
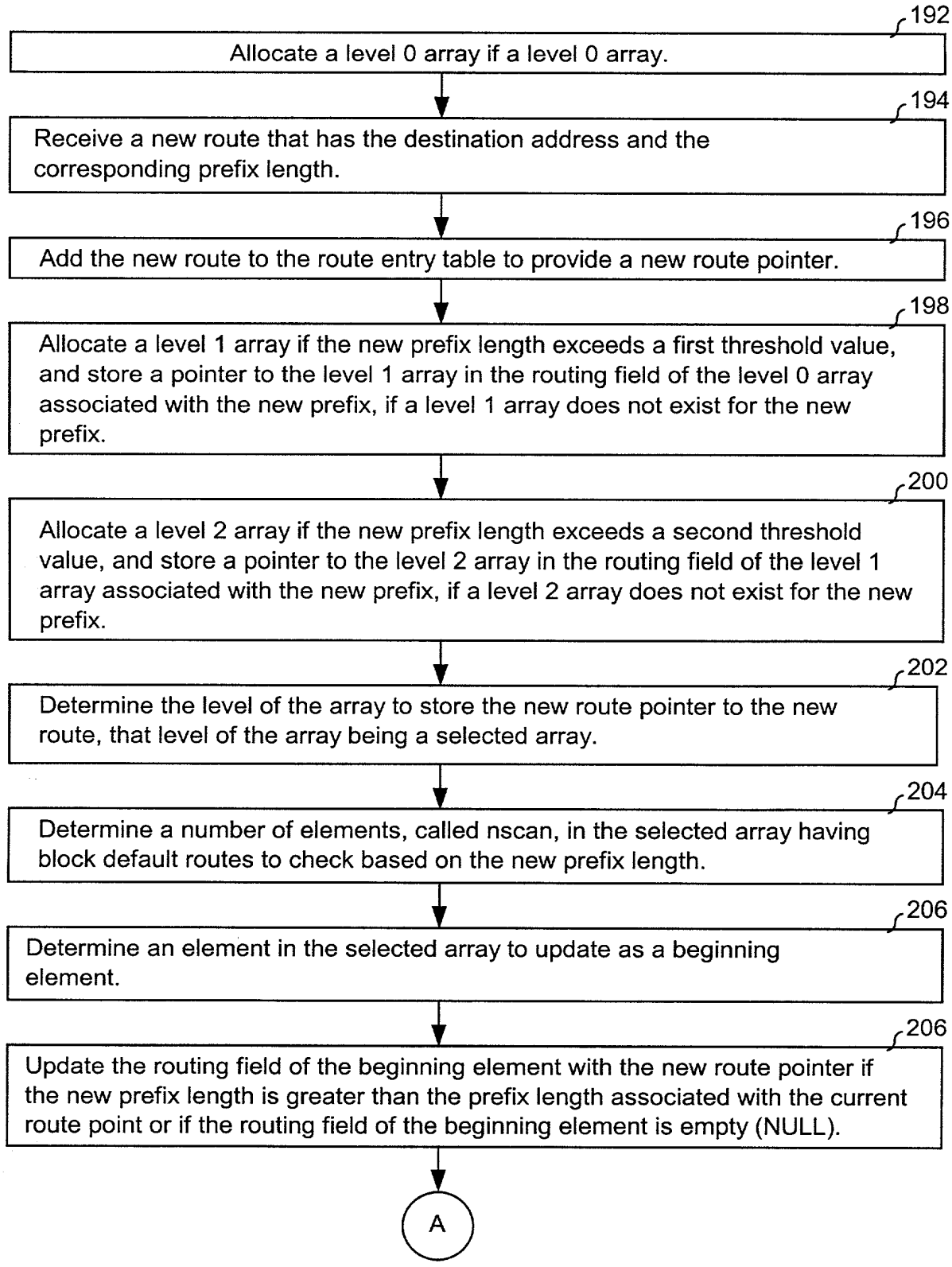
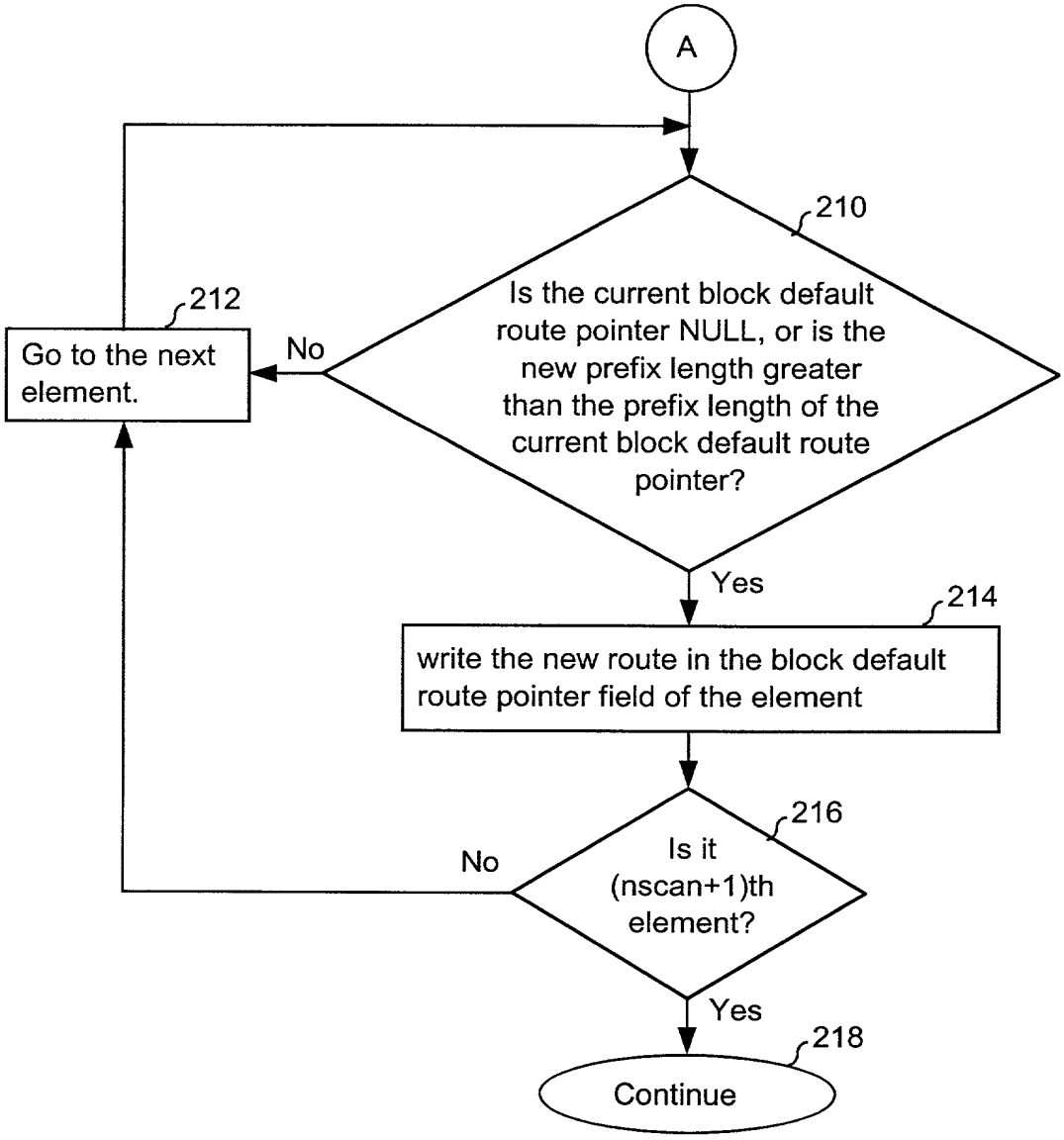

lkup_ip_addr[31:0] = 10/8
route_table_base[31:0]+((256*10)
31:0
63:32
180
Element #
pBlkDef
pRoutingField
0
2,560
2,561
2,562
2,563
2,815
65,535
NULL
B
Level_1_pointer
Level 0 Array
182
255
Level 1 Array

FIG. 11



Flowchart for Adding a Route

FIG. 12A



Flowchart for Adding a Route (continued)

FIG. 12B

```
addRoute(ipa, plen)
/* ipa: destination address of new route,
   plen: prefix length of new route */

Level 0, level 1 and level 2 arrays are allocated and next level route pointers in the
level 0 and level 1 routes are updated.

    array[] = the array to which the new route is added
    begin   = getIndex(ipa,plen)  /* determine which array and element of that
                                     array stores the new route pointer as a route */
    nScan   = getNscan(plen)      /* determine a number of elements to scan */

    /* Update the array with the new route */
    array[begin].pRoutingField = pointer to the new route

    /* Update the block default route */
    i = begin + 1                 /* i points to the next element */
    While nScan-- > 0
        If plen > prefix length of the route pointed by array[i].pBlkdef then
            array[i].pBlkDef = pointer to the new route
        i = i + 1
```

Pseudo code for adding a route

FIG. 13

Memory Write Sequence for Route D Inserted in Level 1 Array

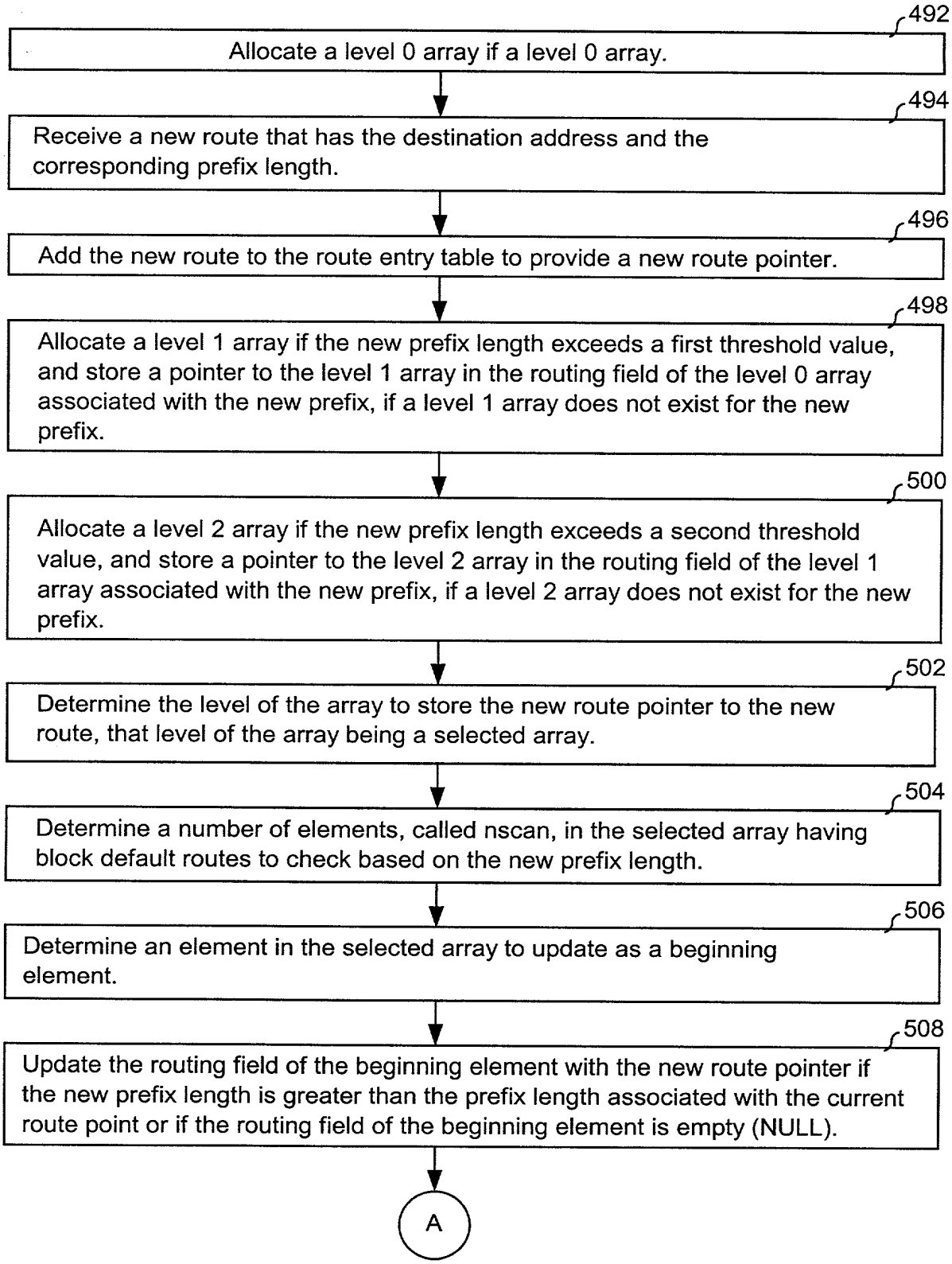
Element 0	Write D in Routing Field
Element 1	Write D in Default Route
Element 2	Write D in Default Route
Element 3	Write D in Default Route
Element 4	Write D in Default Route
Element 8	Write D in Default Route
Element 9	Write D in Default Route
Element 10	Write D in Default Route
Element 11	Write D in Default Route
⋮	⋮

FIG. 14

Memory Write Sequence for Route D Deleted from Level 1 Array

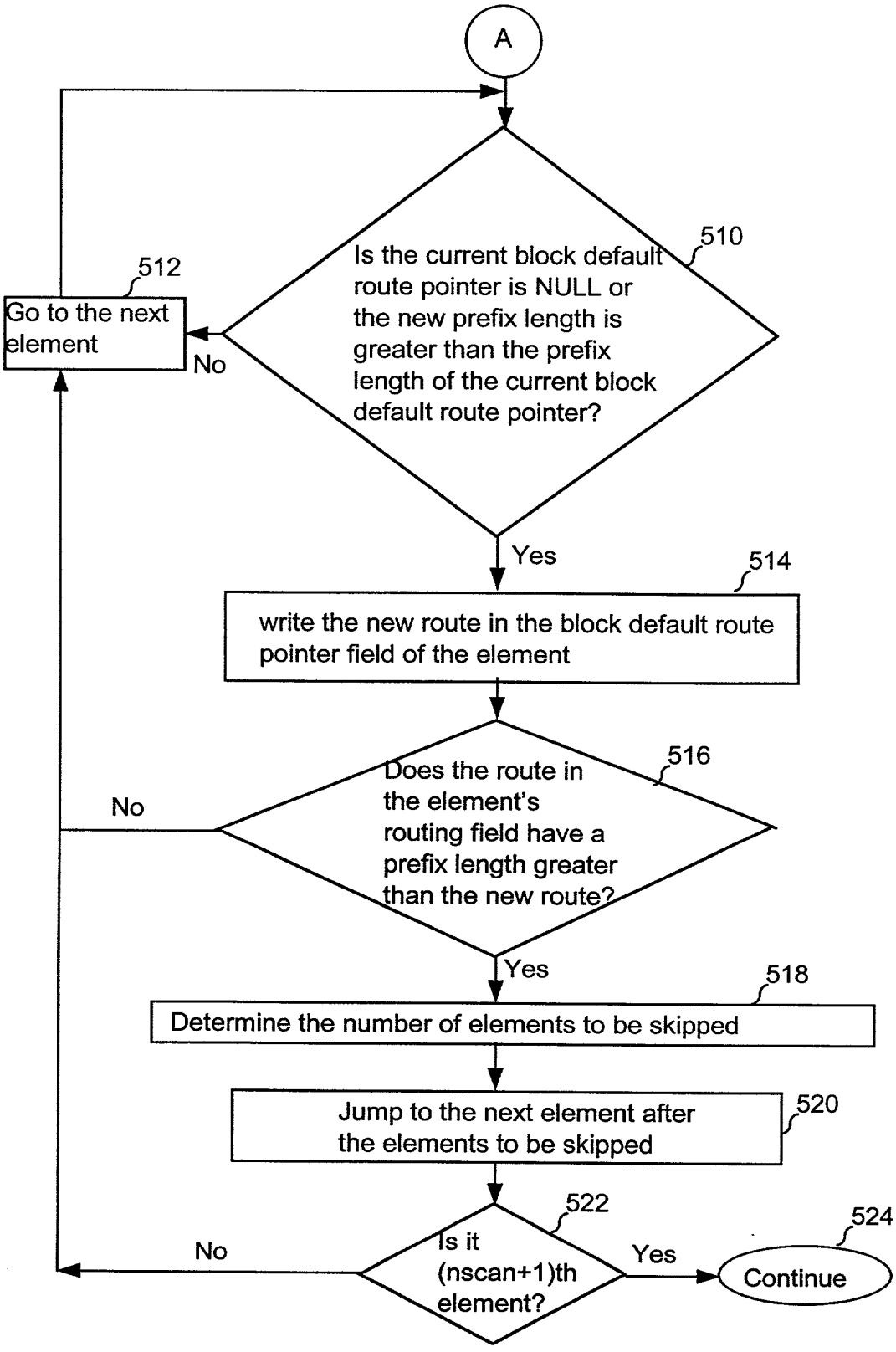
Element 0	Delete D from Routing Field
Element 1	Delete D from Default Route
Element 2	Delete D from Default Route
Element 3	Delete D from Default Route
Element 4	Delete D from Default Route
Element 8	Delete D from Default Route
Element 9	Delete D from Default Route
Element 10	Delete D from Default Route
Element 11	Delete D from Default Route
⋮	⋮

FIG. 17



Flowchart for Adding a Route with Automatic Skipping

FIG. 15A



Flowchart for Adding a Route with Autmatic Skipping (continued)

FIG. 15B

```
addRoute(ipa, plen)
/* ipa: destination address of new route,
   plen: prefix length of new route */

Level 0, level 1 and level 2 arrays are allocated and next level route pointers in the
level 0 and level 1 routes are updated.

    array[] = the array to which the new route is added
    begin   = getIndex(ipa,plen)  /* determine which array and element of that
                                     array stores the new route pointer as a route */
    nScan   = getNscan(plen)      /* determine a number of elements to scan */

    /* Update the array with the new route */
    array[begin].pRoutingField = pointer to the new route

    /* Update the block default route */
    i = begin + 1                 /* i points to the next element */
    While nScan-- > 0
        If plen > prefix length of the route pointed by array[i].pBlkdef then
            array[i].pBlkDef = pointer to the new route
        i = i + 1
    If array[i].pRoutingField = Not Null then
        nSkip = getNscan (plen) + 1       /* get number of elements
                                             to be skipped */
    i = i + nSkip             /* jump to the next element after skipping */
```

Pseudo Code for Enhanced Route Addition with Automatic Skipping

FIG. 16

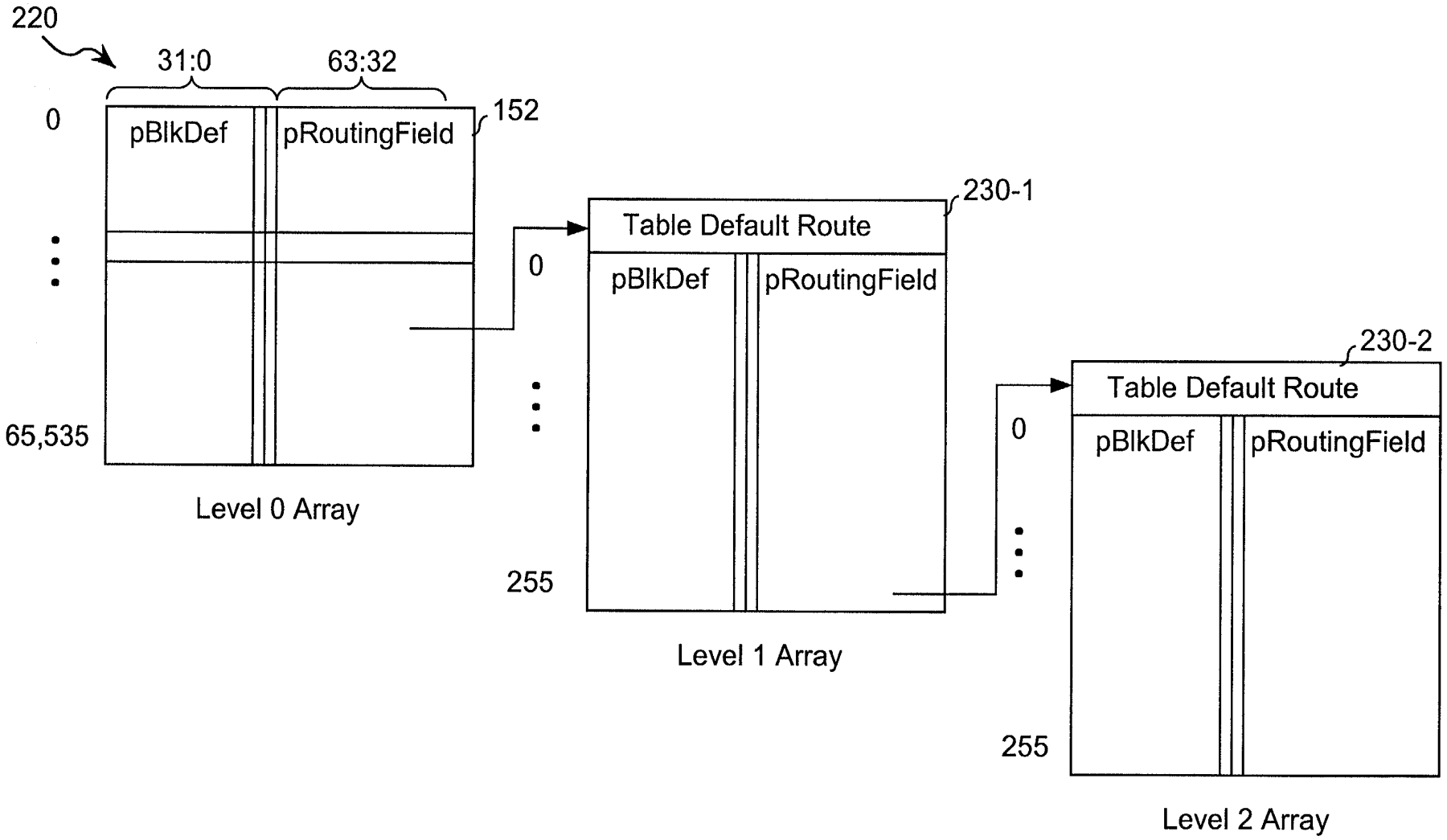

220
31:0
63:32
0
pBlkDef
pRoutingField
152
65,535
Level 0 Array
230-1
Table Default Route
0
pBlkDef
pRoutingField
255
Level 1 Array
230-2
Table Default Route
0
pBlkDef
pRoutingField
255
Level 2 Array

FIG. 18

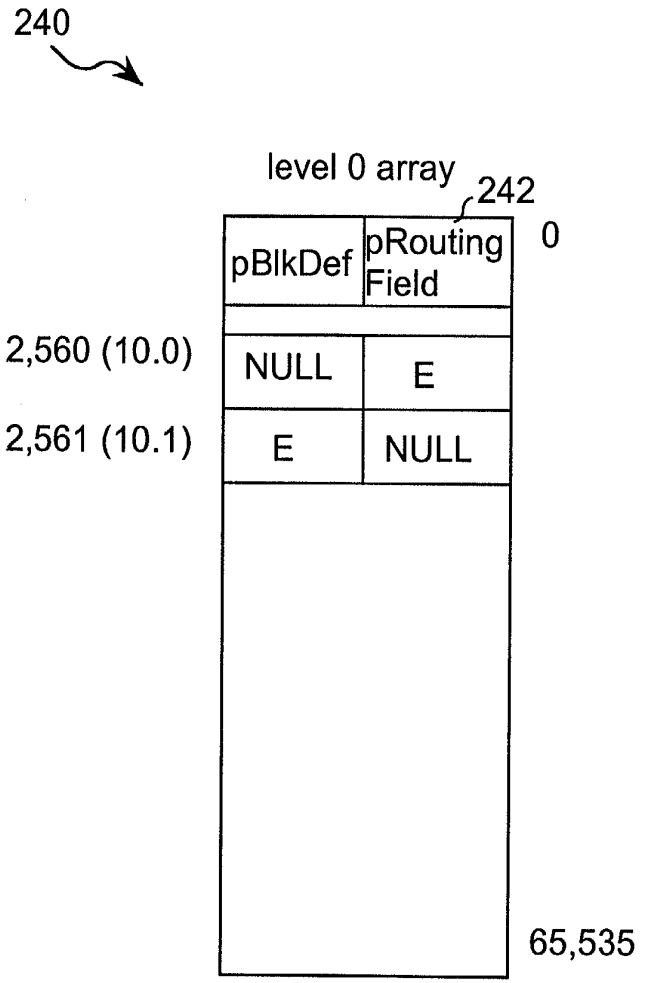


FIG. 19A

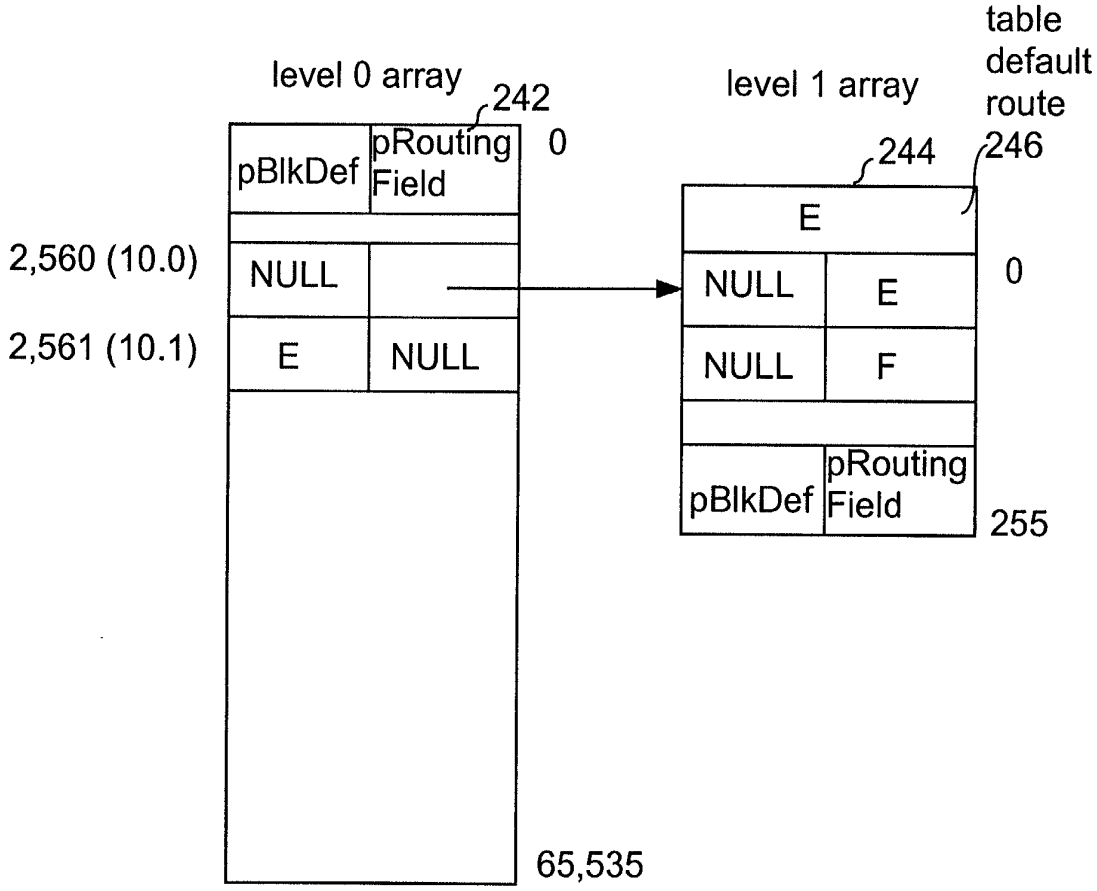
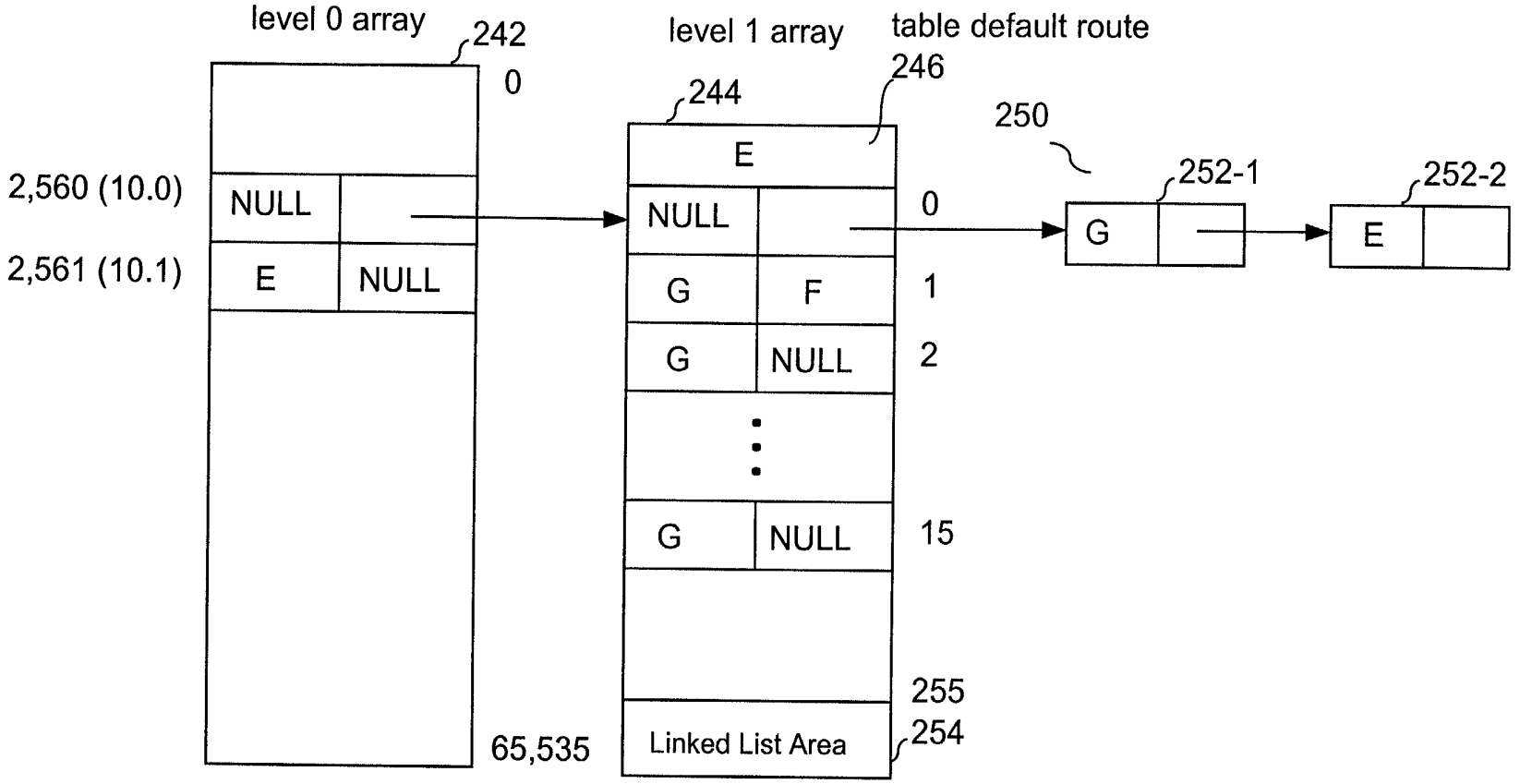


FIG. 19B



Overlapping Routes

FIG. 20

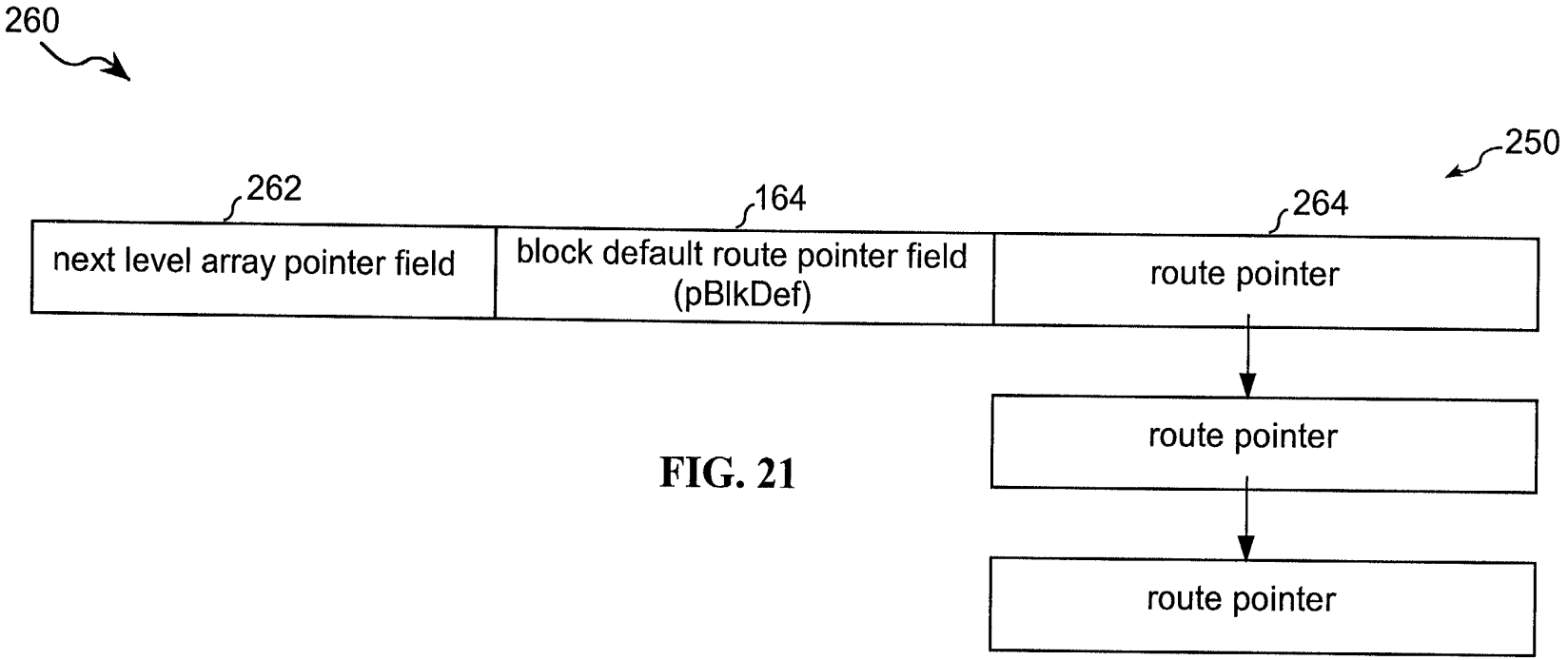

260
250
262
164
264
next level array pointer field
block default route pointer field (pBlkDef)
route pointer
route pointer
route pointer

FIG. 21

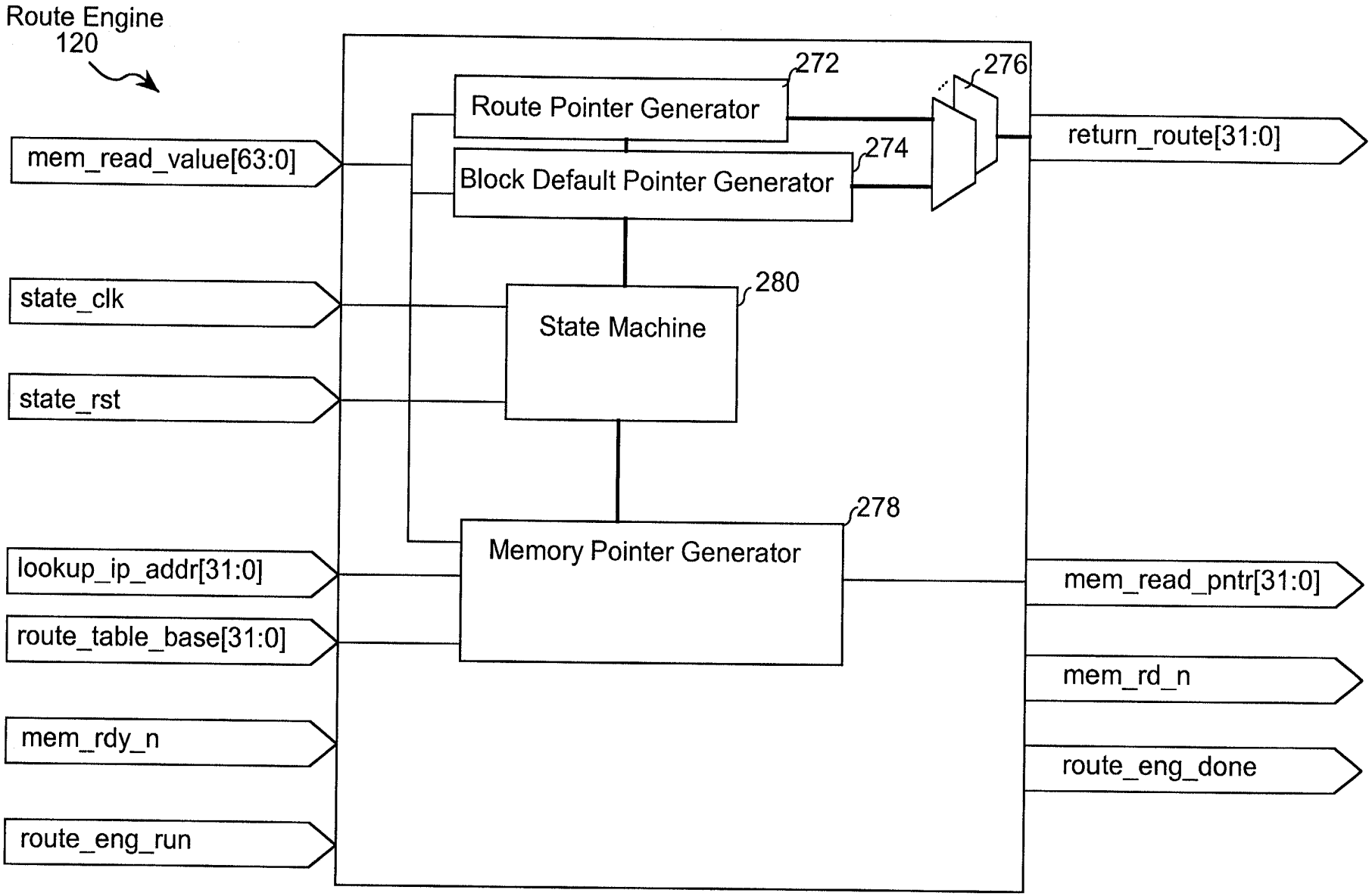

Route Engine
120
272
276
Route Pointer Generator
274
Block Default Pointer Generator
mem_read_value[63:0]
return_route[31:0]
state_clk
280
State Machine
state_rst
278
Memory Pointer Generator
lookup_ip_addr[31:0]
mem_read_pntr[31:0]
route_table_base[31:0]
mem_rd_n
mem_rdy_n
route_eng_done
route_eng_run

FIG. 22

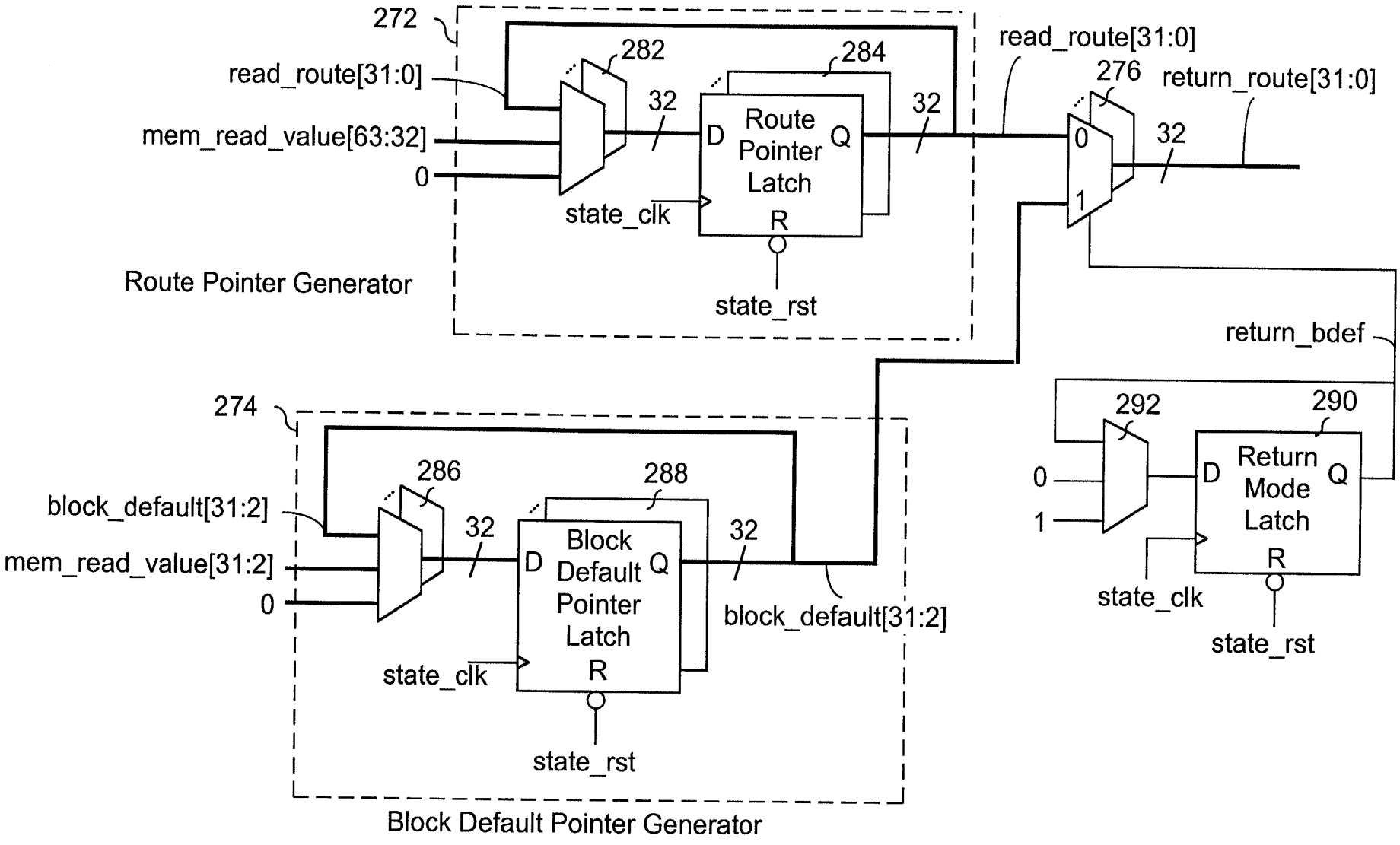

272
read_route[31:0]
mem_read_value[63:32]
0
282
32
D
Route
Pointer
Latch
Q
R
284
32
state_clk
state_rst
Route Pointer Generator
read_route[31:0]
276
return_route[31:0]
0
1
32
return_bdef
274
block_default[31:2]
mem_read_value[31:2]
0
286
32
D
Block
Default
Pointer
Latch
Q
R
288
32
state_clk
state_rst
block_default[31:2]
Block Default Pointer Generator
292
290
0
1
D
Return
Mode
Latch
Q
R
state_clk
state_rst

FIG. 23

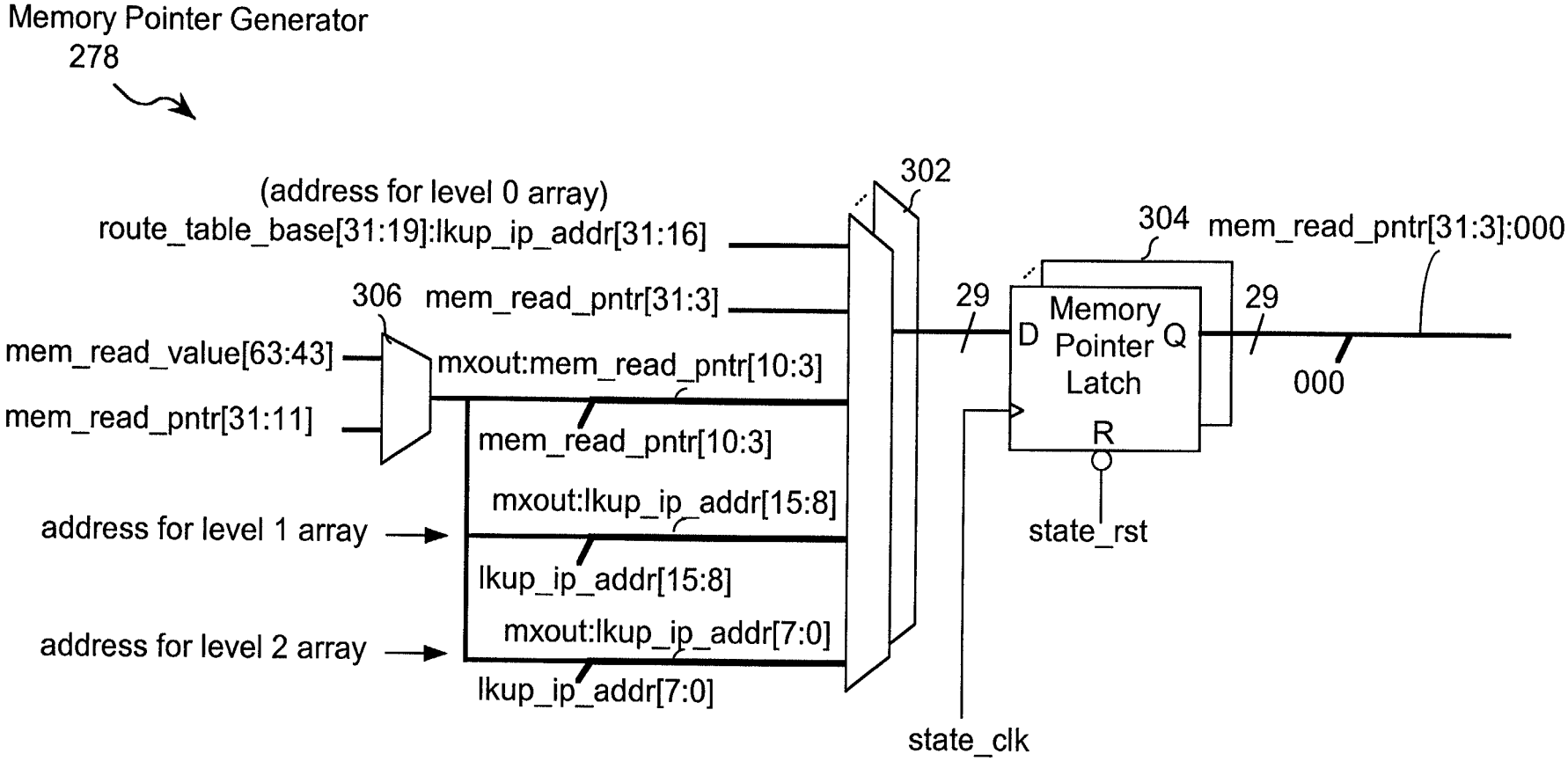

Memory Pointer Generator
278
(address for level 0 array)
route_table_base[31:19]:lkup_ip_addr[31:16]
302
304
mem_read_pntr[31:3]:000
306
mem_read_pntr[31:3]
29
29
Memory
Pointer
Latch
D
Q
R
mem_read_value[63:43]
mxout:mem_read_pntr[10:3]
mem_read_pntr[31:11]
mem_read_pntr[10:3]
000
mxout:lkup_ip_addr[15:8]
address for level 1 array
state_rst
lkup_ip_addr[15:8]
mxout:lkup_ip_addr[7:0]
address for level 2 array
lkup_ip_addr[7:0]
state_clk

FIG. 24

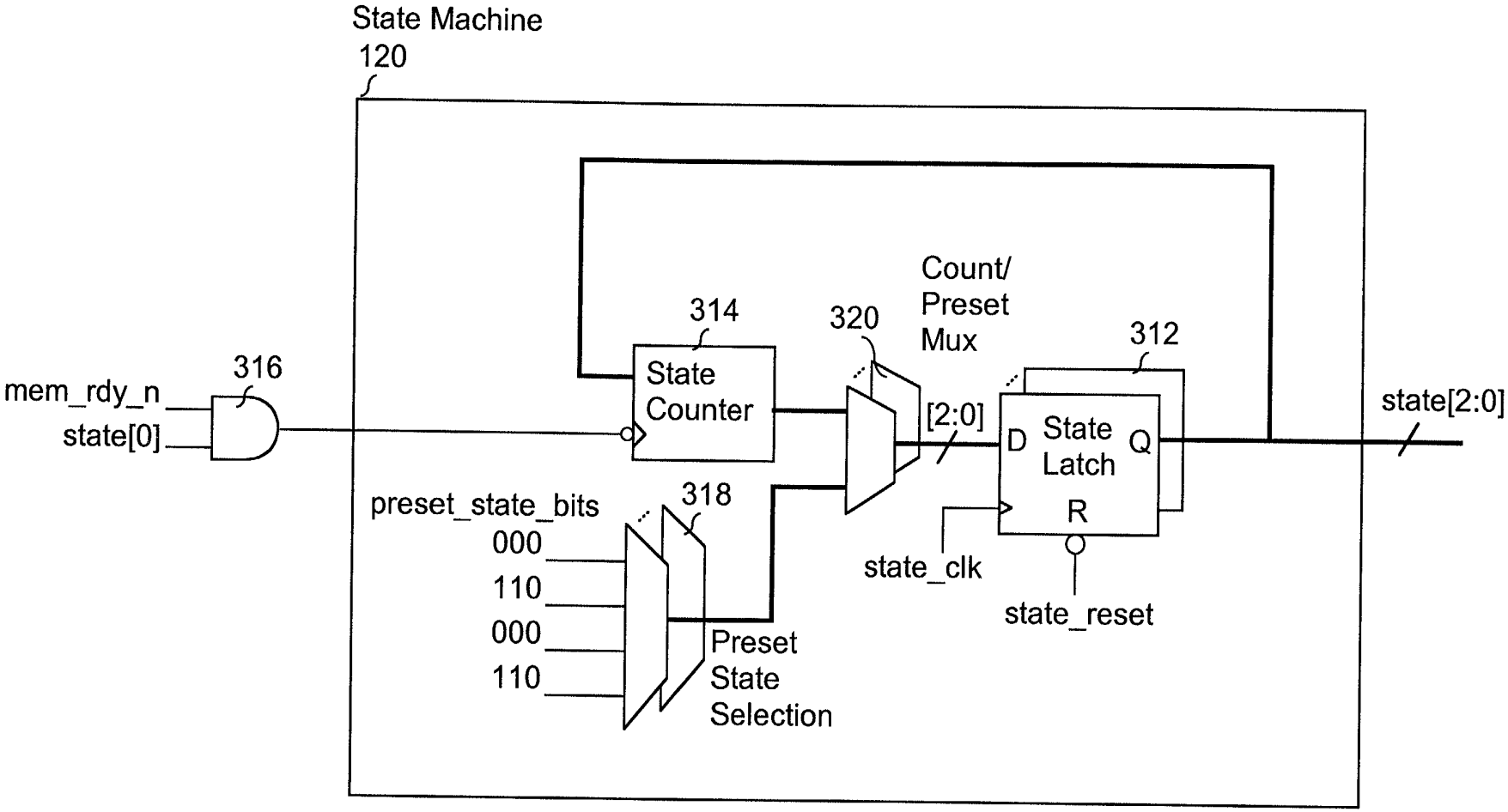

State Machine
120
314
320
Count/
Preset
Mux
312
316
mem_rdy_n
state[0]
State
Counter
[2:0]
D
State
Latch
Q
R
state[2:0]
preset_state_bits
318
000
110
000
110
Preset
State
Selection
state_clk
state_reset

FIG. 25

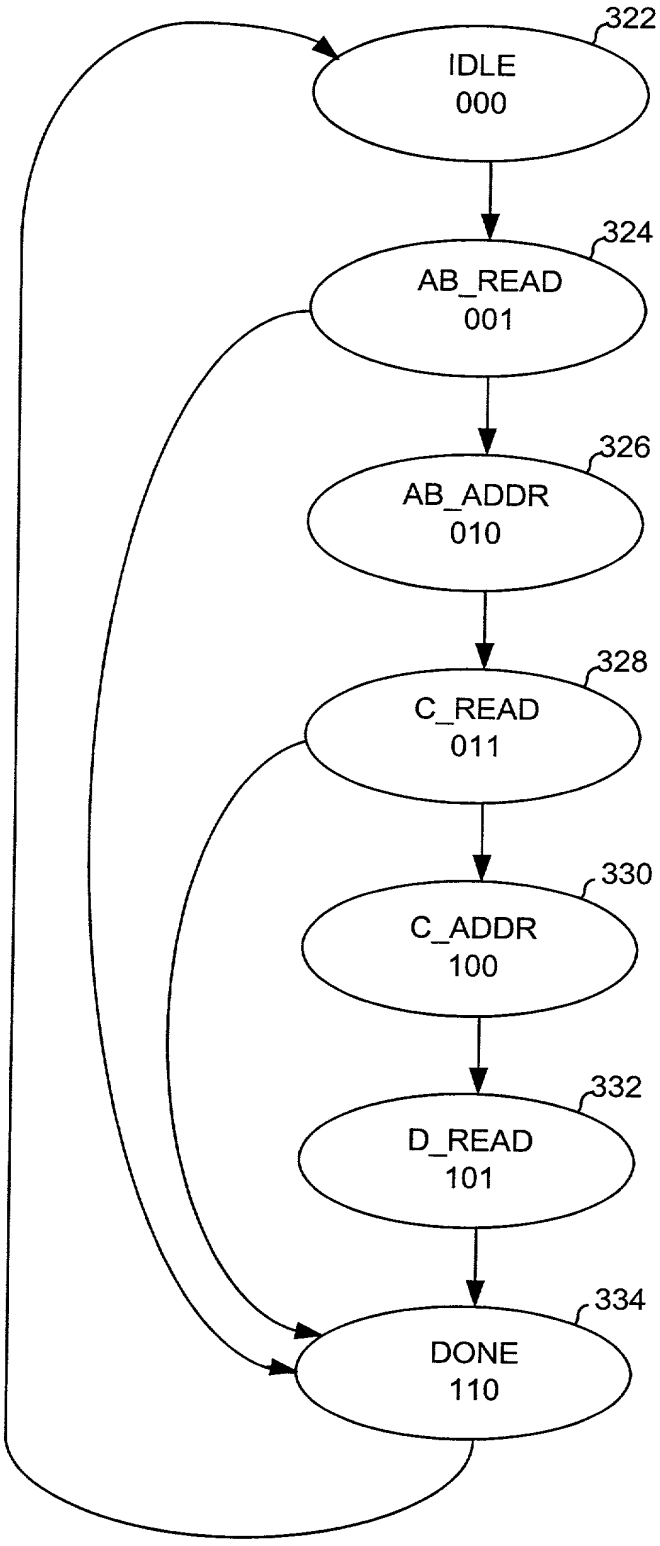

322
IDLE
000
324
AB_READ
001
326
AB_ADDR
010
328
C_READ
011
330
C_ADDR
100
332
D_READ
101
334
DONE
110

FIG. 26